

Design and Performance Analysis of a Two Loop Control for a PWM dc-dc Buck Converter

R.D. Bhagiya¹, R.M. Patel²

Section: Research Paper, Product Type: Journal Paper
Volume-6, Issue-10, Page no. 183-188, Oct-2018

CrossRef-DOI: <https://doi.org/10.26438/ijcse/v6i10.183188>

Online published on Oct 31, 2018

Copyright © R.D. Bhagiya, R.M. Patel . This is an open access article distributed under the Creative Commons Attribution License, which permits unrestricted use, distribution, and reproduction in any medium, provided the original work is properly cited.

Citation

IEEE Style Citation: R.D. Bhagiya, R.M. Patel, "Design and Performance Analysis of a Two Loop Control for a PWM dc-dc Buck Converter", International Journal of Computer Sciences and Engineering, Vol.6, Issue.10, pp.183-188, 2018.

MLA Style Citation: R.D. Bhagiya, R.M. Patel "Design and Performance Analysis of a Two Loop Control for a PWM dc-dc Buck Converter." International Journal of Computer Sciences and Engineering 6.10 (2018): 183-188.

APA Style Citation: R.D. Bhagiya, R.M. Patel, (2018). Design and Performance Analysis of a Two Loop Control for a PWM dc-dc Buck Converter. International Journal of Computer Sciences and Engineering, 6(10), 183-188.

Abstract

In this paper, a two loop control strategy based on frequency domain measures is proposed for a PWM dc-dc buck converter to regulate the output voltage in the presence of input voltage disturbances and load variations. The parameters of inner PI controller and outer PI controller for a specified phase margin and gain cross over frequency are designed using the proposed algorithm. State space averaging technique is used for the modeling of a PWM dc-dc buck converter. The effectiveness of the proposed control strategy is validated using MATLAB/Simulink software for different input voltages and loads.

Key-Words / Index Term

Two loop control, dc-dc buck converter, frequency domain measures, state space averaging technique

References

- [1] A.W. Cristri, and R.F. Iskandar, "Analysis and Design of Dynamic Buck Converter with Change in Value of Load Impedance", Procedia engineering, 170, pp.398-403, 2017.
- [2] R.W. Erickson, and D. Maksimovic, "Fundamentals of power electronics", Springer Science & Business Media, 2007.
- [3] R.D. Middlebrook, and S. Cuk, "A general unified approach to modelling switching-converter power stages", In Power Electronics Specialists Conference, June, 1976, IEEE, pp. 18-34, 1976.

- [4] H. S. Thakur, R. N. Patel, "Assessment of Performance of Integrated Solar PV System with Hybrid Energy Storage System", *International Journal of Computer Sciences and Engineering*, Vol.6, Issue.2, pp.293-302, 2018.
- [5] V. Vorpérian, "Simplified analysis of PWM converters using model of PWM switch. Continuous conduction mode", *IEEE Transactions on aerospace and electronic systems*, Vol.26, Issue 3, pp.490-496, 1990.
- [6] F.L. Luo, and H. Ye, "Small signal analysis of energy factor and mathematical modeling for power dc–dc converters", *IEEE Transactions on Power Electronics*, Vol.22, Issue 1, pp.69-79, 2007.
- [7] V.S.C. Raviraj, and P.C. Sen, "Comparative study of proportional-integral, sliding mode, and fuzzy logic controllers for power converters", *IEEE Transactions on Industry Applications*, Vol.33, Issue 2, pp.518-524, 1997.
- [8] M.M. Garg, Y.V. Hote, and M.K. Pathak, "Design and performance analysis of a pwm dc–dc buck converter using pi–lead compensator", *Arabian Journal for Science and Engineering*, Vol. 40, Issue 12, pp.3607-3626, 2015.
- [9] S. Choudhury, D. Panda, A. Choudhury and P. K. Rout, "A PV based autonomous microgrid performance analysis using PI and adaptive PI controller," 3rd International Conference on Electrical, Electronics, Engineering Trends, Communication, Optimization and Sciences (EEECOS 2016), Tadepalligudem, pp. 1-7, 2016.
- [10] S. Das, M.S. Qureshi, and P. Swarnkar, "Design of integral sliding mode control for DC-DC converters", *Materials Today: Proceedings*, Vol. 5, Issue 2, pp.4290-4298, 2018.
- [11] K.M. Tsang, and W.L. Chan, "Non-linear cascade control of DC/DC buck converter", *Electric power components and systems*, pp.977-989, 2008.
- [12] D. Valério, and J.S. da Costa, "Tuning of fractional PID controllers with Ziegler–Nichols-type rules", *Signal processing*, Vol. 86, Issue 10, pp.2771-2784, 2006.
- [13] B.J. Patella, A. Prodic, A. Zirger, and D., "Maksimovic, High-frequency digital PWM controller IC for DC-DC converters", *IEEE Transactions on Power electronics*, Vol. 18, Issue 2, pp.438-446, 2003.